

AUS920010142US1

Arndt et al.

Method and Apparatus for Isolating Failing Hardware in a PCI Recoverable Error

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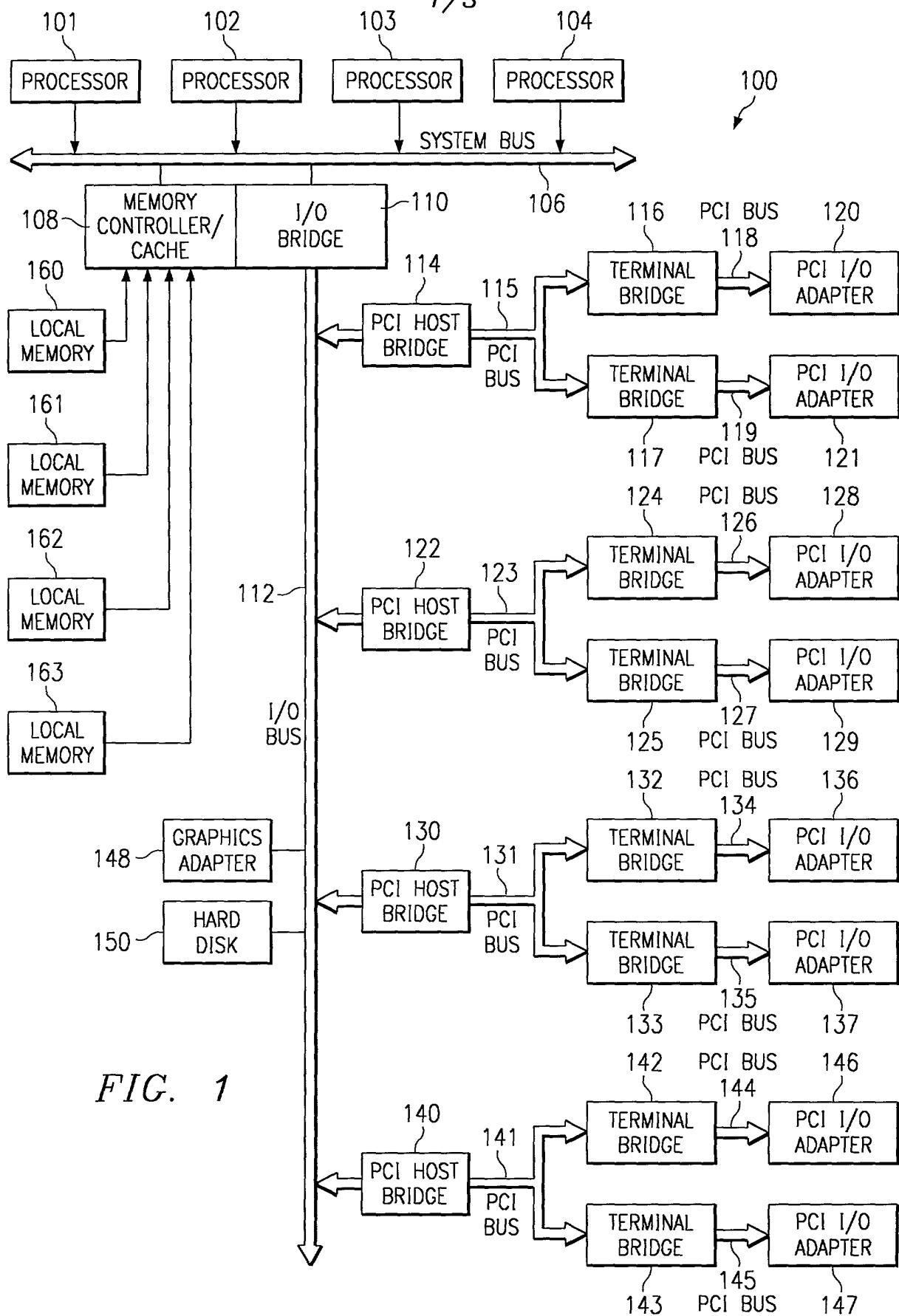


FIG. 1

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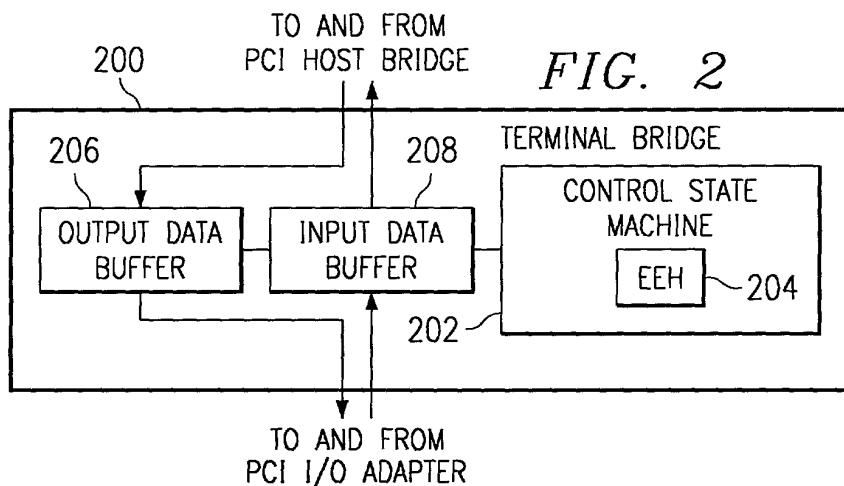


FIG. 3

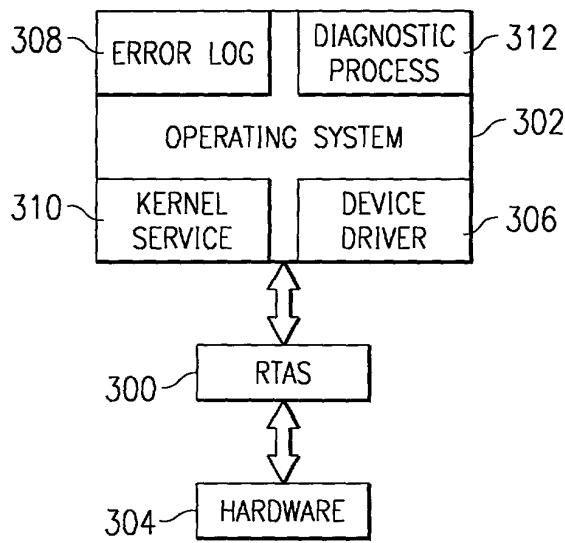


FIG. 6

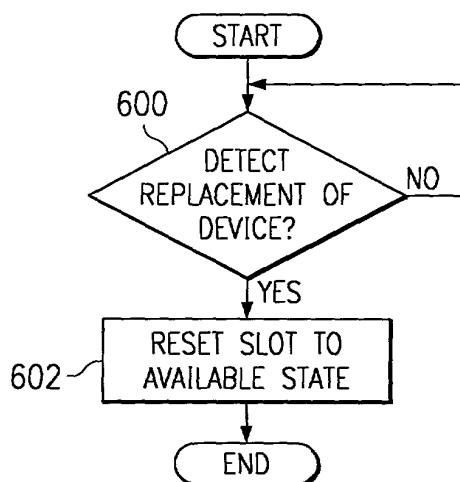


FIG. 2

FIG. 5

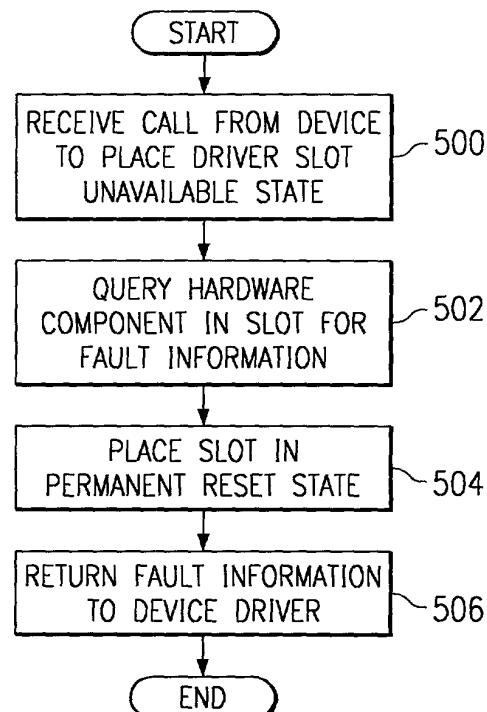


FIG. 4

